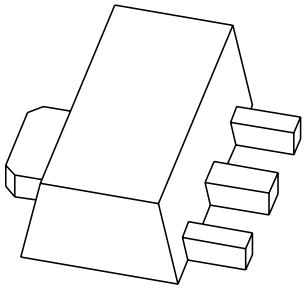
DATA SHEET



PXT4401
NPN switching transistor

Product specification
Supersedes data of 1997 May 07

1999 Apr 14

Philips
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NPN switching transistor PXT4401

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

APPLICATIONS

- Switching and linear amplification in industrial and consumer applications.

DESCRIPTION

NPN switching transistor in a SOT89 plastic package.
PNP complement: PXT4403.

MARKING

TYPE NUMBER	MARKING CODE
PXT4401	p2X

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base

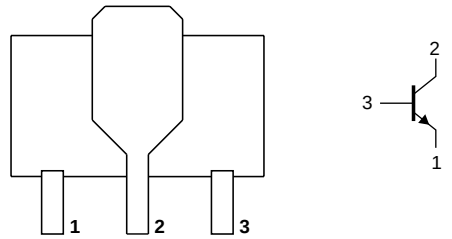


Fig.1 Simplified outline (SOT89) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	60	V
V_{CEO}	collector-emitter voltage	open base	–	40	V
V_{EBO}	emitter-base voltage	open collector	–	6	V
I_C	collector current (DC)		–	600	mA
I_{CM}	peak collector current		–	800	mA
I_{BM}	peak base current		–	200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	1.25	W
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	operating ambient temperature		–65	+150	°C

Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 6 cm^2.
   For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook".*

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j\text{-}a}$	thermal resistance from junction to ambient	note 1	100	K/W
$R_{th\ j\text{-}s}$	thermal resistance from junction to soldering point		20	K/W

Note

1. Device mounted on a printed-circuit board, single-sided copper, tinplated, mounting pad for collector 6 cm^2. For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook".*

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I_{CBO}	collector cut-off current	I_E = 0; V_{CB} = 60 V	–	50	nA
I_{EBO}	emitter cut-off current	I_C = 0; V_{EB} = 6 V	–	50	nA
h_{FE}	DC current gain	V_{CE} = 1 V; (see Fig.2)	20	–	
		I_C = 0.1 mA	20	–	
		I_C = 1 mA	40	–	
		I_C = 10 mA	80	–	
		I_C = 150 mA; note 1	100	300	
		I_C = 500 mA; V_{CE} = 2 V; note 1	40	–	
V_{CEsat}	collector-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; note 1	–	400	mV
		I_C = 500 mA; I_B = 50 mA; note 1	–	750	mV
V_{BEsat}	base-emitter saturation voltage	I_C = 150 mA; I_B = 15 mA; note 1	–	950	mV
		I_C = 500 mA; I_B = 50 mA; note 1	–	1.2	V
C_c	collector capacitance	$I_E = i_e$ = 0; V_{CB} = 5 V; f = 1 MHz	–	8	pF
C_e	emitter capacitance	$I_C = i_c$ = 0; V_{EB} = 500 mV; f = 1 MHz	–	30	pF
f_T	transition frequency	I_C = 20 mA; V_{CE} = 10 V; f =100 MHz	250	–	MHz
Switching times (between 10% and 90% levels); (see Fig.3)					
t_{on}	turn-on time	I_{Con} = 150 mA; I_{Bon} = 15 mA; I_{Boff} = –15 mA	–	35	ns
t_d	delay time		–	15	ns
t_r	rise time		–	20	ns
t_{off}	turn-off time		–	250	ns
t_s	storage time		–	200	ns
t_f	fall time		–	60	ns

Note

1. Pulse test: $t_p \leq 300\ \mu s$; $\delta \leq 0.02$.

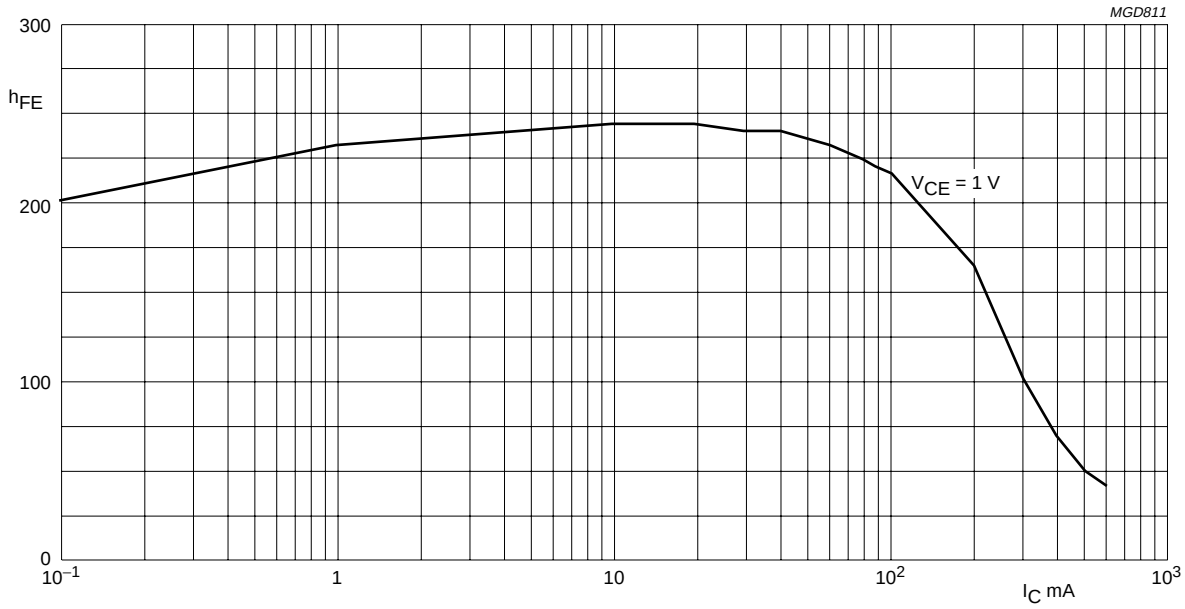
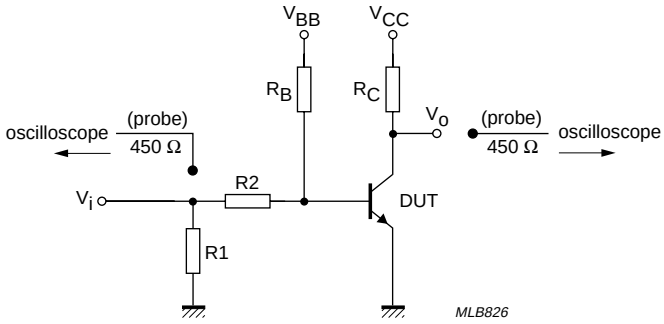


Fig.2 DC current gain; typical values.

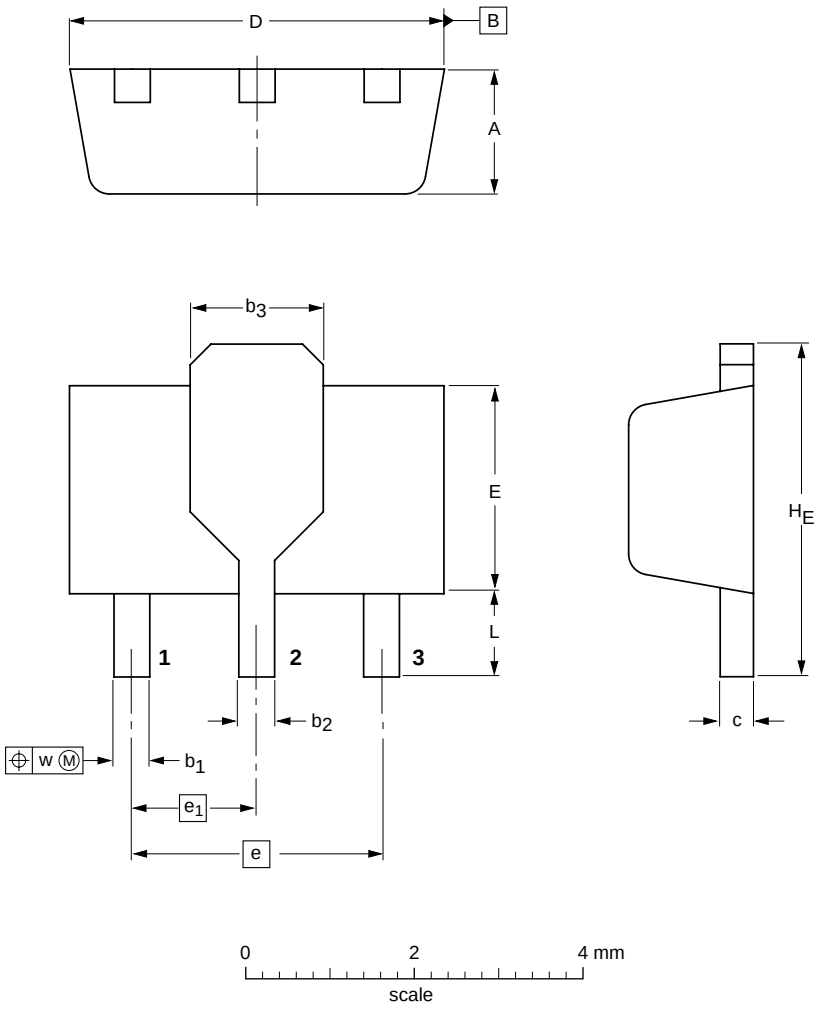


V_i = 9.5 V; T = 500 μs; t_p = 10 μs; t_r = t_f ≤ 3 ns.
R1 = 68 Ω; R2 = 325 Ω; R_B = 325 Ω; R_C = 160 Ω.
V_{BB} = −3.5 V; V_{CC} = 29.5 V.
Oscilloscope: input impedance Z_i = 50 Ω.

Fig.3 Test circuit for switching times.

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads **SOT89**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_1	b_2	b_3	c	D	E	e	e_1	H_E	L min.	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.37	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	0.8	0.13

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT89						97-02-28

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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Printed in The Netherlands 115002/00/03/pp8 Date of release: 1999 Apr 14 Document order number: 9397 750 05626

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